



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of: Long et al.

Attorney Docket No.: SUN1P800/P5254

Patent: 7,086,053.B2

Issued: August 1, 2006

Title: METHOD AND APPARATUS FOR
ENABLING THREADS TO REACH A
CONSISTENT STATE WITHOUT EXPLICIT
THREAD SUSPENSION

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on August 28, 2006 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed:

Aurelia M. Sanchez

**REQUEST FOR CERTIFICATE OF CORRECTION
OF OFFICE MISTAKE
(35 U.S.C. §254, 37 CFR §1.322)**

Certificate

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Attn: Certificate of Correction

SEP 07 2006

of Correction

Dear Sir:

Attached is Form PTO-1050 (Certificate of Correction) at least one copy of which is suitable for printing. The errors together with the exact page and line number where the errors are shown correctly in the application file are as follows:

CLAIMS:

1. In line 2 of claim 5 (column 11, line 33) change "farther comprising" to --further comprising--. This appears correctly in Amendment D as filed on March 20, 2006, on page 3, claim 5, line 2.

SEP 07 2006

Patentee hereby requests expedited issuance of the Certificate of Correction because the error lies with the Office and because the error is clearly disclosed in the records of the Office. As required for expedited issuance, enclosed is documentation that unequivocally supports the patentee's assertion without needing reference to the patent file wrapper.

It is noted that the above-identified errors were printing errors that apparently occurred during the printing process. Accordingly, it is believed that no fees are due in connection with the filing of this Request for Certificate of Correction. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. SUN1P800).

Respectfully submitted,
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SEP 07 2007

program means for saving a snapshot of an indication of a state of the first thread and thereafter setting the state of the first thread to a safe state, wherein the indication of the state of which the snapshot is saved is an indication of whether or not the first thread was consistent;

program means for acquiring a consistent state lock using the first thread;

program means for identifying substantially all threads that are inconsistent, the inconsistent threads being included in the multiple threads;

program means for altering the state of the substantially all threads that are inconsistent to a consistent state;

program means for notifying the first thread when the state of the substantially all threads that are inconsistent have been altered to be consistent;

program means for restoring the indication of the state of the first thread from the snapshot; and

program means for releasing the consistent state lock using the first thread.

5. (previously presented) A computing apparatus as recited in claim 4, the program executing thereon further comprising:

program means for performing a garbage collection after releasing the consistent state lock using the first thread.

6. (previously presented) A computing apparatus as recited in claim 5, the program executing thereon further comprising:

program means for notifying the substantially all threads that have been altered to be consistent that the garbage collection has been performed.

7. (previously presented) A computer readable medium having embodied thereon a computer program for requesting a consistent state in a computing environment using a first thread, the computing environment including multiple threads, the multiple threads including the first thread, the computer program comprising:

computer program code for saving a snapshot of an indication of a state of the first thread and thereafter setting the state of the first thread to a safe state, wherein the indication of the state of which the snapshot is saved is an indication of whether or not the first thread was consistent;

computer program code for acquiring a consistent state lock using the first thread;

computer program code for identifying substantially all threads that are inconsistent, the inconsistent threads being included in the multiple threads;

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(Also Form PT-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 7,086,053 B2

Page 1 of 1

DATED : August 1, 2006

INVENTOR(S) : Long et al.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In the Claims:

In line 2 of claim 5 (column 11, line 33) change "farther comprising" to --further comprising--.

MAILING ADDRESS OF SENDER:

PATENT NO. 7,083,053 B2

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